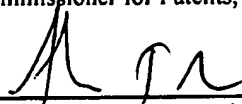


THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 09/932,216
Applicant : Yang
Filed : August 17, 2001
TC/A.U. : 2816
Examiner : Terry D. Cunningham
Title: BOOTSTRAP MODULE FOR MULTI-STAGE CIRCUIT
Docket No. : 2550/111
Customer No. : 5542

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service in an envelope as first class mail with sufficient postage addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on May 5, 2005.



Alexander J. Smolenski, Jr.

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. §1.116-EXPEDITED PROCEDURE
EXAMINING GROUP 2816

Response F

Dear Sir:

In response to the final office action dated March 7, 2006, Applicants submit the following remarks:

Claims begin on page 2 of this paper, and

Remarks begin on page 7 of this paper.

An Appendix is attached following page 10 of this paper, including: page 55 of Weste, Neil and Eshraghian, Kamran, *Principles of CMOS VLSI Design*, Addison-Wesley, Reading, Massachusetts, 1985; and page 1-6 of Rung-Bin Lin, Chapter 1: Introduction to CMOS Circuits, http://vlsi.cse.yzu.edu.tw/education/vlsi_design/chapter1.pdf.